

## **IN THE SPECIFICATION**

Please amend the paragraph beginning on page 8, line 4, as follows:

The oversampling receiver 200 may oversample the incoming data in one of a variety of ways. As an example, the oversampling receiver 200 may use a sampling clock that is faster (*e.g.*, 3 times, 4 times, 6 times, etc.) than the rate of the incoming data to sample the incoming data. As another example, a plurality of sampling clocks having substantially the same frequency as the input data signal may be phase-shifted to oversample the incoming data. As mentioned above, for example, the sampling block 210 205 uses, in one embodiment, three phase-shifted sampling clocks to sample each incoming bit three times to generate the s0, s1, and s2 samples.